

FIG. 1A

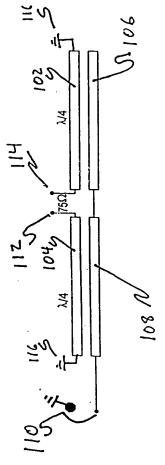
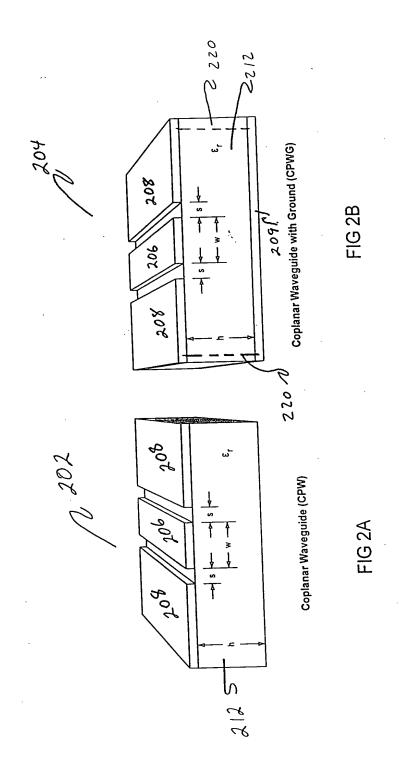


Figure 1B



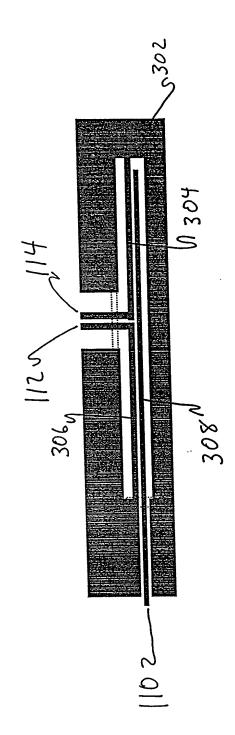


Figure 3

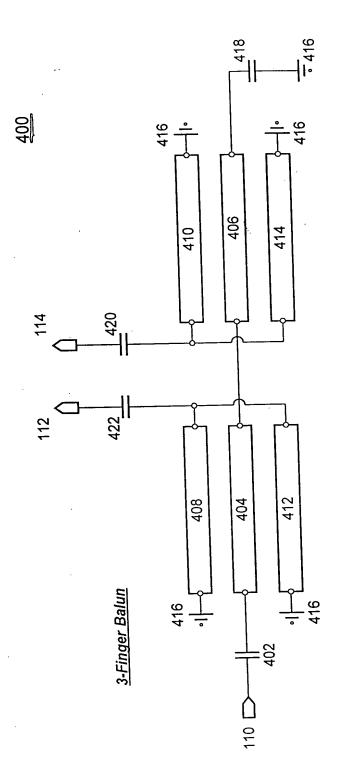


FIG. 4A

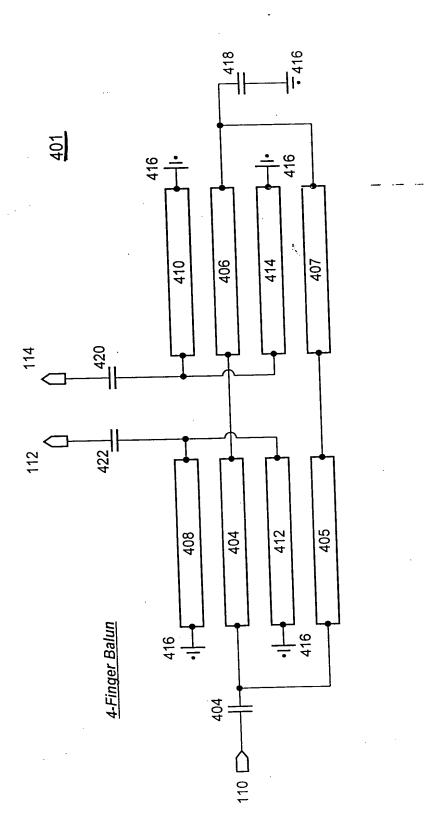
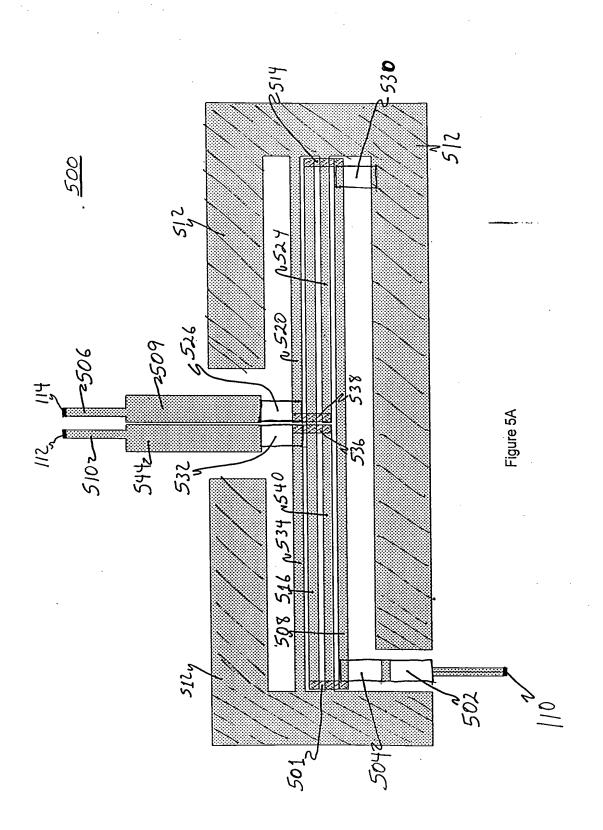
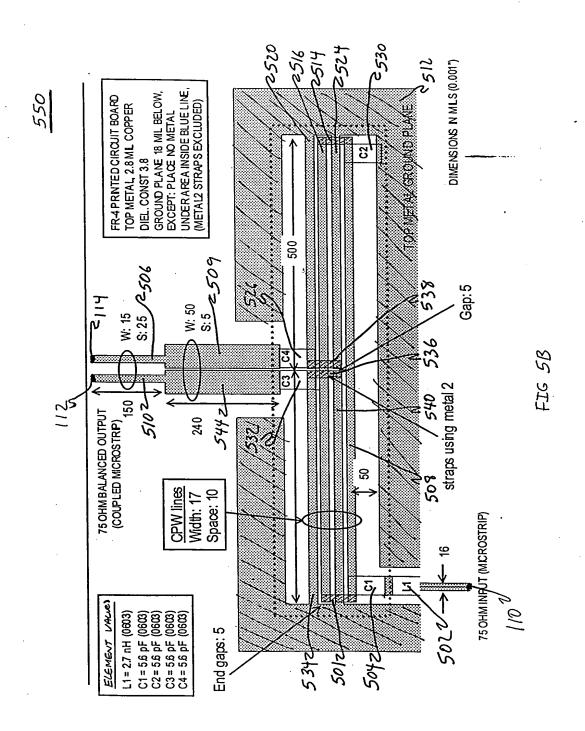
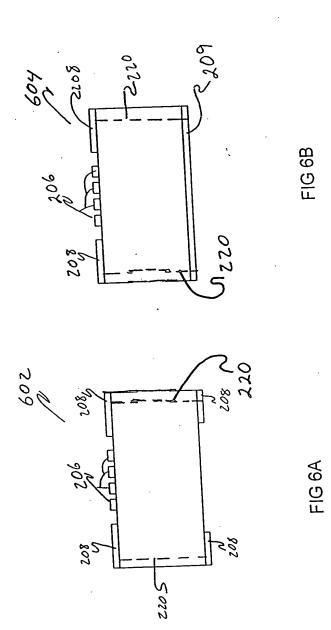


FIG. 4B

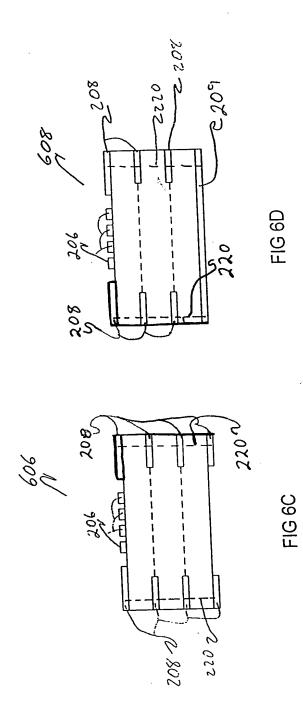




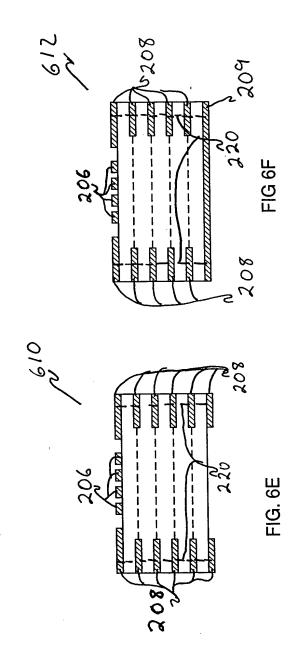
2 Layer Printed Circuit Board

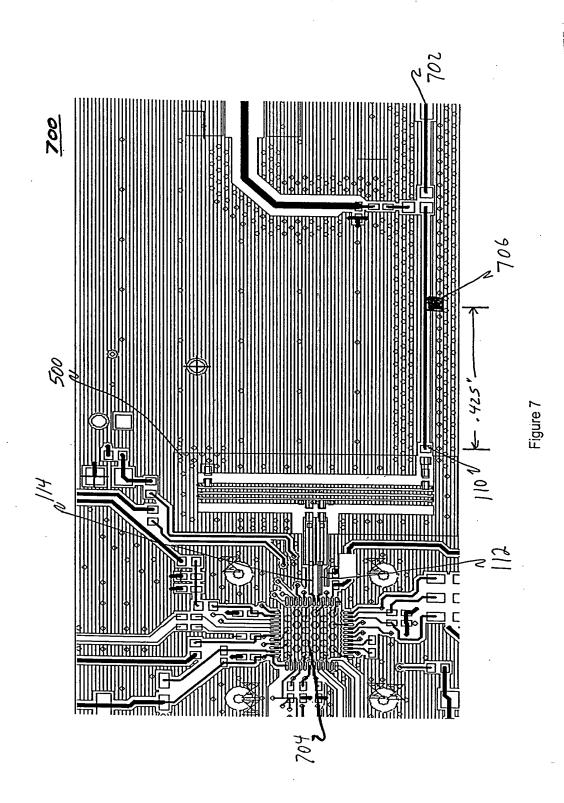


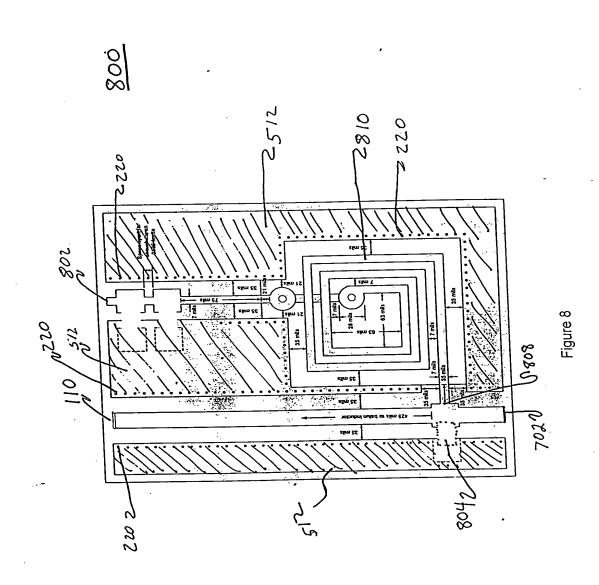
4 Layer Printed Circuit Board

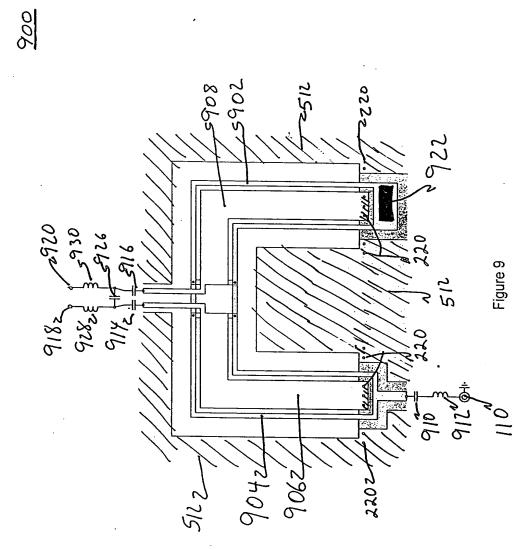


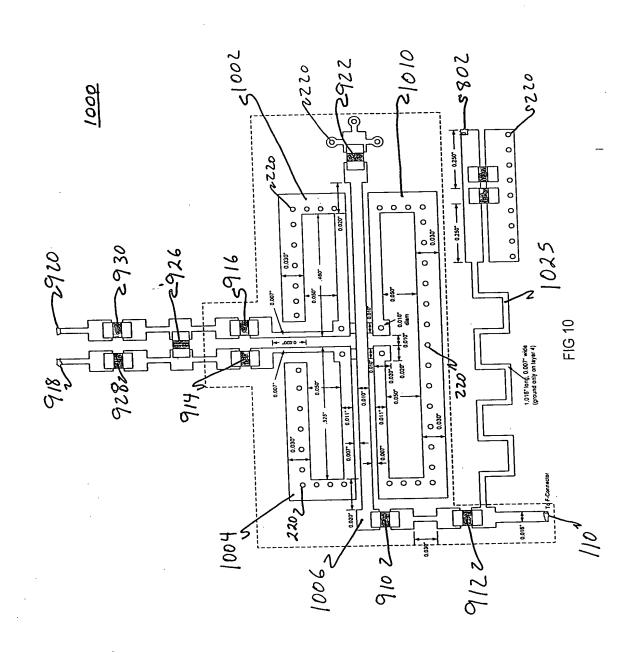
6 Layer Printed Circuit Board











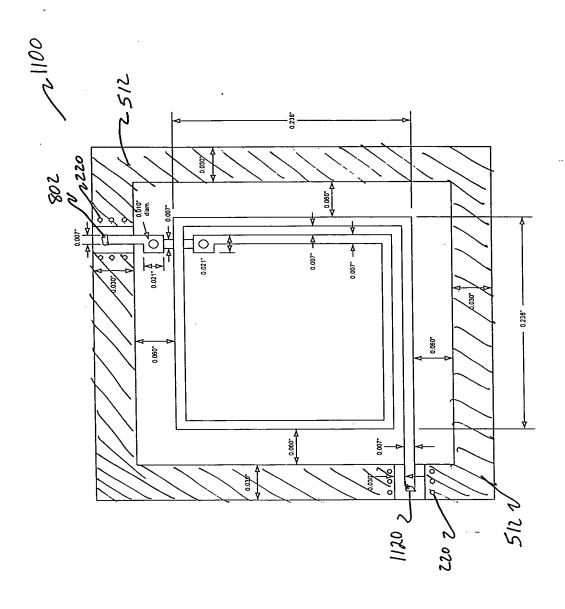


FIG 11

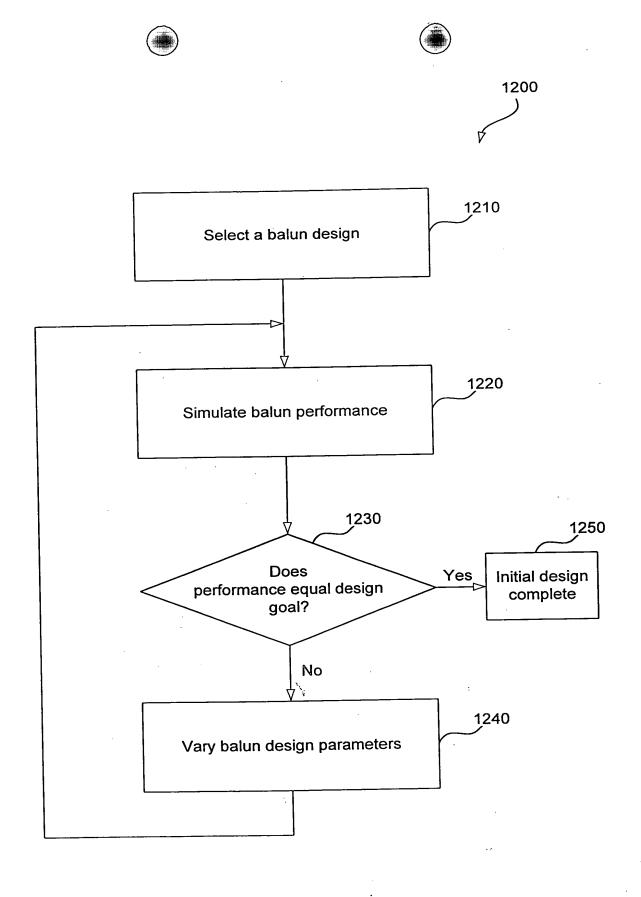


FIG. 12

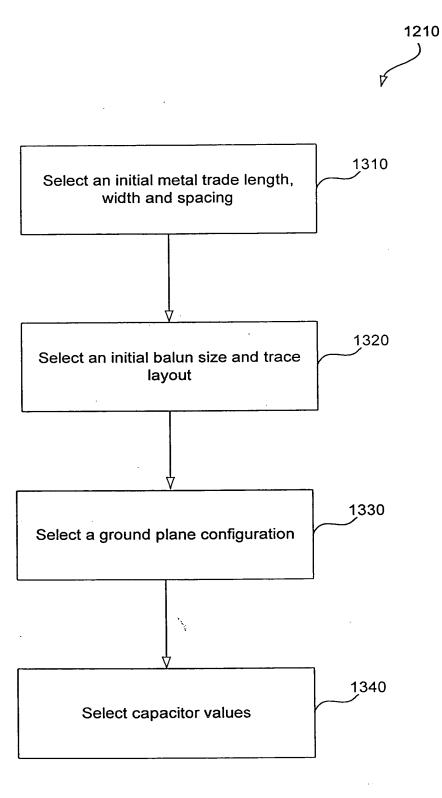


FIG. 13

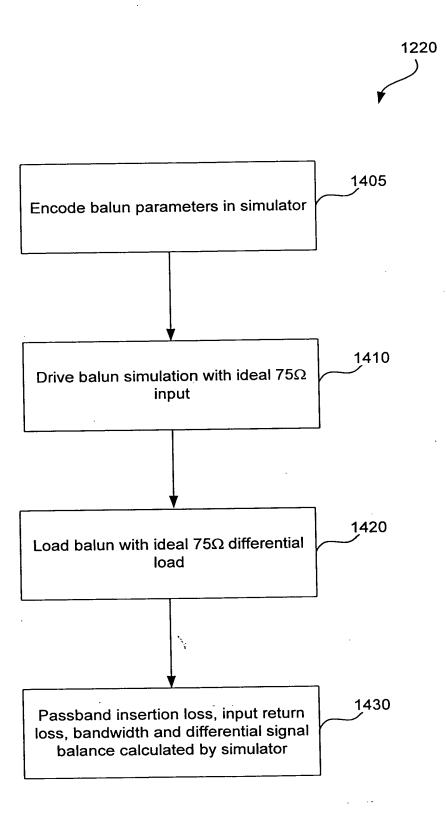


FIG. 14

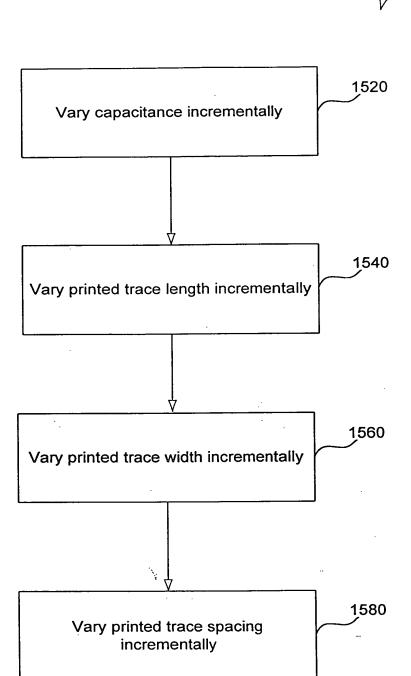


FIG. 15

1240

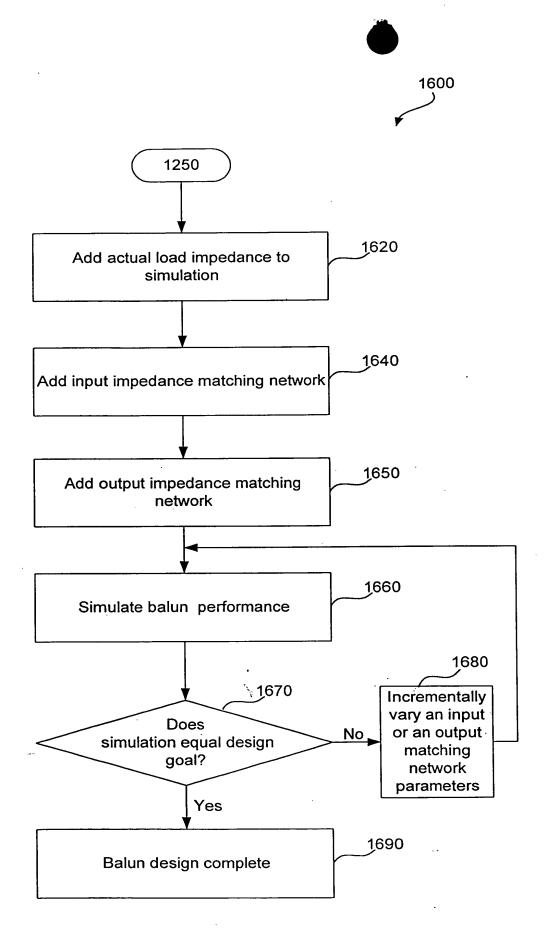


FIG. 16